

modulation photogates (1, 2) relative to each other and relative to a potential of the accumulation gates (4, 5) corresponding to a desired modulation function, characterised in that there are provided a plurality of modulation photogates (1, 2) and accumulation gates (4, 5) being formed of long narrow parallel strips which group-wise form a PMD-pixel, wherein the accumulation gates are reading-out diodes, wherein the accumulation gates of a (PMD-) pixel are divided into at least two groups of accumulation gates, each group being connected to a common readout line and means are provided for determining the difference of the signals received from the readout lines of the said at least two groups of accumulation gates.

2. (Fourth amended) An apparatus Apparatus as set forth in claim 1, characterized in that the width of the modulation photogates is greater than the width of the accumulation gates.

3. (Third amended) Apparatus as set forth in claim 2 characterized in that the width of the modulation photogates is of the order of magnitude of a wavelength or, in particular and charachterized in that, for the remote infrared range, the width of the modulation photogates is of an order of magnitude less than the wavelength of the electromagnetic waves to which the modulation photogates are sensitive.

4. (Five amended) Apparatus as set forth in one of claims 2, through 3, and 27 characterized in that a strip length of the modulation photogates (1, 2) and the accumulation gates (4, 5) is more than ten times the wavelength of the electromagnetic waves to which the modulation photogates are sensitive.

5. (Five amended) Apparatus as set forth in one of claims 2, through 3, and 27 characterized in that there are provided a plurality of modulation photogates in paired parallel mutually

juxtaposed relationship, wherein each of the modulation photogates (1, 2) of such a pair is connected to another modulation connection so that the modulation photogates (1, 2) are modulatable in push-pull relationship, wherein a respective accumulation gate (5, 4) is arranged between a pair of modulation photogates (1, 2) and a next adjacent further pair of modulation photogates (2, 1) and wherein the modulation photogates (1, 2) of the two pairs, which are immediately adjacent to a respective accumulation photogate (4, 5), are connected or electrically joined to the modulation connections in such a way that their modulation occurs respectively in push-pull mode.

6. (Four amended) Apparatus as set forth in one of claims 2, through 3, and 27 characterized in that a plurality of modulation connections (m_1, m_2, m_3) are arranged at substantially equal spacings along the length of the strips and are connected to the modulation photogates (1, 2).

7. (Five amended) Apparatus as set forth in one of claims 2, through 3, and 27 characterized in that the modulation photogates immediately adjoining the accumulation gates (4, 5), on a side towards the accumulation gates, include a contacting strip of high conductivity and of no or very low transparency.

8. (Five amended) Apparatus as set forth in one of claims 2, through 3, and 27 characterized in that the apparatus has one or more pixel units, wherein each pixel unit is substantially square-shaped and comprises a plurality of strip-shaped modulation gates (1, 2) and accumulation gates (4, 5), which pixel units are assembled to form a pixel, wherein the strip directions of adjacent pixel units are perpendicular to each other, wherein adjacent pixel units are at different modulation voltages, and wherein transversely with respect to the strip direction of a respective pixel unit the ends of the strip-shaped modulation gates are

defined by at least one respective modulation photogate (1, 2) which adjoins a next accumulation gate (4, 5).

9. (Twice-Amended) Apparatus as set forth in claim 8 characterized in that the electrical connections for the accumulation gate are provided at a respective end of the strips of a pixel, wherein each second accumulation gate is connected to a first respective one of two reading-out lines (corresponding to K+) and the other accumulation gates are connected to the respective other one of the connection lines (corresponding to K-), wherein the reading-out lines lead to an evaluation circuit.

10. (Third-Amended) Apparatus as set forth in claim 8 characterized in that two pixel units (10, 10') are arranged with their strips parallel and in directly mutually juxtaposed relationship so that mutually immediately adjacent modulation photogates which define mutually juxtaposed ends or sides of the two pixel units (10, 10') form a pair of modulation photogates (1, 2) which are modulatable selectively in push-pull mode or phase-displaced relationship, whereby either a single pixel element of double the size of one of the two pixel units is formed or two independent measurement procedures, being of an in-phase signal and a quadrature signal, are possible with the two pixel units.

11. (Third-Amended) Apparatus as set forth in claim 8 characterized in that there are four pixel units arranged in a rectangle, wherein the strips of pixel units which are respectively disposed in diagonally opposite relationship in the rectangle extend parallel to each other, while the strips of immediately adjacent pixel units extend perpendicularly to each other, and wherein modulation connections are connected in such a way that modulation of adjacent pixel units (10) can be effected in phase-shifted relationship through 90°.

12. (Twice-Amended) Apparatus as set forth in claim 11 characterized in that each of the four pixel units (10) is respectively of a substantially square shape and the four pixel units are assembled to form a square.

13. (Twice-Amended) Apparatus as set forth in claim 12 characterized in that the four pixel units are selectively combined individually (4-quadrant operation) or doubly in diagonal relationship (2-quadrant operation) or in quadruple relationship (1-quadrant operation), wherein in the case of 4-quadrant operation and 2-quadrant operation the gradient or normal vector of the surface of the four pixel units is additionally evaluated.

14. (Third-Amended) Apparatus as set forth in claim 2 characterized in that the modulation photogates and the accumulation gates and associated signal evaluation peripheral equipment and modulation peripheral equipment are produced in part on-chip and in part as a multi-chip module using CMOS-technology or BICMOS-technology.

15. (Third-Amended) Apparatus as set forth in claim 2 characterized in that arranged over the modulation photogates (1, 2) are strip lenses which focus substantially all the electromagnetic waves incident on the surface of a pixel element exclusively on to the modulation photogates (1, 2).

16. (Twice-Amended) Apparatus as set forth in claim 2 characterized in that a plurality of PMD-pixels are arranged in a linear or matrix array.

17. (Four amended) Apparatus as set forth in claim 2 characterized in that in a linear or matrix array has both PMD-pixels with 3D-functionality and also conventional CMOS-pixels with 2D-

functionality used in mixed mode, wherein various adjacent items of pixel information are passed to a data-fusioning and interpolating device for reconstruction of a depth image.

18. (Third-Amended) Apparatus as set forth in claim 17 characterized in that associated with each said PMD-pixel is a microlens which concentrates the electromagnetic waves incident on the array onto the photosensitive surface of that PMD-pixel.

19. (Three amended) ~~Use of an apparatus~~ Apparatus as set forth in claim 2 characterized in that the apparatus ~~is used as a~~ provides photosensitive image-recording element in a 3-D-vision camera.

20. (Three amended) ~~Use of the apparatus~~ Apparatus as set forth in claim 2 characterized in that the apparatus ~~is used in~~ provides optical signal processing as a frequency- and phase-sensitive mixing or correlation element for signal acquisition, processing and noise suppression.

21. (Five- amended) A method of operating an apparatus as set forth in claim 8 characterized in that a scene of which an image is to be produced is illuminated with a light modulated in accordance with a modulation function, wherein the modulation photogates (1, 2) are modulated with bipolar or push-pull modulation function and wherein, selectively for half of a 2-quadrant or 4-quadrant pixel, of the pixels 90° phase-shifted modulation is effected in the case of sine modulation or a bit width in the case of rectangular modulation or a chip width in the case of PN-modulation of the modulation photogate potentials, wherein the accumulation gates of a (PMD-) pixel are divided into at least two groups of accumulation gates, each group being connected to a common readout line and means are provided for determining the difference of the signals received from the readout lines of the said at least two groups of accumulation gates.

22. (Four amended) ~~Use of an apparatus~~ Apparatus as set forth in claim 2 characterized in that the apparatus ~~is used in~~ provides an optical PLL-circuit or DLL-circuit which circuits are highly integrated and used in light barrier arrangements with each including various modulation modes.

23. (Four amended) ~~Use of an apparatus~~ Apparatus as set forth in any one of claims 2 and 3 characterized in that the apparatus ~~is used in~~ provides an optical PLL- or DLL-circuit with a 2Q-PMD-DLL on the basis of an IQ-PMD-receiver, with PN-modulation, wherein digital PN-encoded data signals are used for multi-channel selection, multi-target detection and for highest sensitivity in phase transit time resolution, wherein a difference output voltage is formed as a difference of quantitative differences of photocurrents as $U_{\Delta} = \text{const} \cdot (|i_a - i_b| - |i_c - i_d|)$ and is fed back by way of a loop filter or a digital regulator as a control parameter of a voltage-controlled multivibrator to a chip frequency and wherein a data signal of a PN-encoded 1/0-data sequence is regenerated by means of a recovered word clock by a procedure whereby in a summing amplifier (41) a sum of the differences of photocurrents as $U_{\Sigma} = \text{const} \cdot (|i_a i_b| + |i_c i_d|)$ is respectively formed over a PN-word length by means of a short-term integrator contained in the summing amplifier.

24. (Amended) Apparatus as set forth in claim 16 characterized in that associated with each said PMD-pixel is a microlens which concentrates the electromagnetic waves incident on the array onto the photosensitive surface of that PMD-pixel.

25. (Original) Apparatus as set forth in one of claims 2 through 3 characterized in that a strip length of the modulation photogates (1, 2) and the accumulation gates (4, 5) is more than fifty

times the wavelength of the electromagnetic waves to which the modulation photogates are sensitive.

26. (Amended) Apparatus as set forth in claim 10, characterized in that the apparatus is used in provides an optical PLL- or DLL-circuit with a 2Q-PMD-DLL on the basis of an IQ-PMD-receiver, with PN-modulation, wherein digital PN-encoded data signals are used for multi-channel selection, multi-target detection and for highest sensitivity in phase transit time resolution, wherein a difference output voltage is formed as a difference of quantitative differences of photocurrents as $U_{\Delta} = \text{const} \cdot (|i_a - i_b| - |i_c - i_d|)$ and is fed back by way of a loop filter or a digital regulator as a control parameter of a voltage-controlled multivibrator to a chip frequency and wherein a data signal of a PN-encoded 1/0-data sequence is regenerated by means of a recovered word clock by a procedure whereby in a summing amplifier (41) a sum of the differences of photocurrents as $U_{\Sigma} = \text{const} \cdot (|i_a - i_b| + |i_c - i_d|)$ is respectively formed over a PN-word length by means of a short-term integrator contained in the summing amplifier.

27. (Original) Apparatus as set forth in claim 2 further characterized wherein said accumulation gates (4,5) are of photosensitive material which is shaded.